

Title (en)  
Production of solder masked electric circuit boards

Title (de)  
Herstellung von lötmaskierten Leiterplatten

Title (fr)  
Fabrication de plaquettes de circuit imprimé masquées contre la soudure

Publication  
**EP 0480365 B1 19960110 (EN)**

Application  
**EP 91117125 A 19911008**

Priority  
JP 27095990 A 19901008

Abstract (en)  
[origin: EP0480365A2] The present invention provides a process for forming a solder mask on an electric circuit board (3) without any defects. The process comprises forming an electric circuit on a substrate, coating a negative type photosensitive solder resist (2) on the obtained electric circuit board, and then exposing to light through a negative film (4) followed by developing, characterized in that the negative type photosensitive solder resist is electrodepositable and said coating of the solder resist is conducted by electrocoating the resist on an electroconductive transfer substrate (1) which is different from the electric circuit board and then transferring the coated film onto the electric circuit board (3).

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IPC 8 full level  
**H05K 3/28** (2006.01); **H05K 3/00** (2006.01)

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