

Title (en)

Video system controller with a row address override circuit.

Title (de)

Steuerlogik für ein Videosystem mit einer Schaltung, welche die Zeilenadresse ausser Kraft setzt.

Title (fr)

Contrôleur de système vidéo avec un circuit de dépassement de l'adresse de ligne.

Publication

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Application

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- US 63338484 A 19840723
- US 63338584 A 19840723
- US 63338684 A 19840723
- US 63338784 A 19840723
- US 63338884 A 19840723
- US 63338984 A 19840723

Abstract (en)

The present invention is a video system which includes a data processor (1), such as a microprocessor, for processing data, a video memory (5) for storing data from the data processor corresponding to an image to be displayed, a display (11), such as a raster scan cathode ray tube, for displaying the image data stored in the video memory means, and a video system controller (3) connected to the video memory (5) for controlling the transfer of data from the video memory (5) to the display (11) and between the data processor (1) and the video memory (5). The video memory (5) is preferably a multiport dynamic random access memory including an addressable memory array. The video system controller (3) performs a number of functions including refresh of the dynamic random access memory, multiplexing of the various access requests of the video memory and control of the blanking interval of the display. This is accomplished by having a first portion which operates synchronously with the video memory (5) and a second portion which operates synchronously with the data processor (1). The transfer operations in the video system controller are preferably controlled through the use of a programmable state machine which manipulates inputs in a logic array. <IMAGE>

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