

Title (en)

WAKE-UP CIRCUIT ARRANGEMENT FOR A MICROPROCESSOR.

Title (de)

AUFWECK-SCHALTUNGSANORDNUNG FÜR EINEN MIKROPROZESSOR.

Title (fr)

CIRCUIT D'ACTIVATION POUR UN MICROPROCESSEUR.

Publication

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Application

**EP 90910560 A 19900725**

Priority

DE 3926178 A 19890808

Abstract (en)

[origin: WO9102303A1] In a wake-up circuit arrangement for a microprocessor (  $\mu$  C) which controls electrical devices, in particular in a motor vehicle, peripheral components (15, 17) of the microprocessor (  $\mu$  C) can be disconnected from a voltage supply arrangement (7) and the microprocessor (  $\mu$  C) can be switched into an idle or stop mode. In order to execute a queueing program, the microprocessor (  $\mu$  C) can be switched out of this mode into its operating mode by an external switching signal which actuates an electronic wake-up circuit (20, 20') associated with the microprocessor (  $\mu$  C). To ensure reliable switching of the microprocessor (  $\mu$  C) independently of the type and duration of the control signal, the switching signals are fed in the form of potential jumps to a timing pulse generator stage (C1, R8, IC1) the output (23) of which switches a pulse (25) of defined length to the interrupt input (INT) of the microprocessor (  $\mu$  C). The microprocessor (  $\mu$  C) then exits the idle or stop mode and switches on the voltage supply arrangement (7) for the peripheral components (15, 17).

IPC 1-7

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IPC 8 full level

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