

Title (en)

Column addressing circuit for a matrix display.

Title (de)

Spaltenadressierschaltung für eine Matrixanzeige.

Title (fr)

Circuit d'adressage des colonnes d'un écran matriciel.

Publication

**EP 0488851 A1 19920603 (FR)**

Application

**EP 91403104 A 19911119**

Priority

FR 9014784 A 19901127

Abstract (en)

The present invention relates to a column addressing circuit for a matrix display comprising NL lines and nc columns. This circuit is constituted by at least one RAM memory (10) organised into NL lines of nc blocks of m bits making it possible to store the data corresponding to a display and linked, at its output, to an output circuit (11) with nc stages and nc outputs directly controlling the nc columns of the active matrix display (1). Application especially to liquid crystal displays. <IMAGE>

Abstract (fr)

La présente invention concerne un circuit d'adressage des colonnes d'un écran matriciel comportant NL lignes et nc colonnes. Ce circuit est constitué par au moins une mémoire RAM (10) organisée en NL lignes de nc blocs de m bits permettant de stocker les informations correspondant à un écran et reliée en sortie à un circuit de sortie (11) à nc étages et nc sorties commandant directement les nc colonnes de l'écran à matrice active (1). Application notamment aux écrans à cristaux liquides. <IMAGE>

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**G09G 3/36**

IPC 8 full level

**G02F 1/133** (2006.01); **G09G 3/20** (2006.01); **G09G 3/36** (2006.01); **G09G 5/395** (2006.01)

CPC (source: EP)

**G09G 3/2011** (2013.01); **G09G 3/3688** (2013.01); **G09G 5/395** (2013.01); **G09G 2310/027** (2013.01)

Citation (search report)

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- [A] CONFERENCE RECORD OF THE 1988 INTERNATIONAL DISPLAY RESEARCH CONFERENCE. 4-6 OCT. 1988 JUN-ICHI OHWADA ET AL.: PERIPHERAL CIRCUIT INTEGRATED POLY-SI TFT LCD WITH GRAY SCALE ; REPRESENTATION - P.215-219

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EP1146501A4; US7180495B1; US8159440B2

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