Title (en)

Synchronizing circuit

Title (de)

Synchronisationsschaltung

Title (fr)

Circuit de synchronisation

Publication

EP 0491090 B1 19970312 (EN)

Application

EP 90203446 A 19901218

Priority

EP 90203446 A 19901218

Abstract (en)

[origin: EP0491090A1] A synchronizing circuit to synchronize a digital input signal (DIN) with a clock signal (CK1) includes a detection circuit (DC) which checks if a present (SA) sample of a clock signal (CK3) being synchronized with the digital input signal, is equal to the previous (SB) sample, both samples being taken at an interval equal to the period (T) of the clock signal synchronized with the output signal. When the samples differ, the detection circuit generates a phase adjustment signal (CLR), which triggers a phase adjustment circuit (PAC) to ensure a return to synchronism by phase shifting the signal (ES) controlling the sampling of the digital input signal. <IMAGE>

IPC 1-7

H04L 7/033

IPC 8 full level

H03L 7/00 (2006.01); **H04L 7/00** (2006.01); H04L 7/02 (2006.01)

CPC (source: EP KR US)

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DOCDB simple family (publication)

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