

Title (en)

Driver circuit for liquid crystal display.

Title (de)

Treiberschaltung für eine Flüssigkristallanzeige.

Title (fr)

Circuit d'entraînement pour une affichage à cristaux liquides.

Publication

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Application

EP 91122321 A 19911227

Priority

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Abstract (en)

A driver circuit for a liquid crystal display comprises a multiplexer (MPX) which selectively outputs m/2 kinds of drive source voltages among m kinds of different drive source voltages (VLC1 SIMILAR VLCm), necessary for m/2 display gradations, according to a frame selection signal (VFRM) supplied from a frame selection terminal (FRM). The number of transistor switches (T11 SIMILAR Tk(m/2)) which are divided into a plurality of switch groups can thus be reduced to one half that in the conventional LCD driver circuit. The LCD driver circuit comprises operational amplifiers (OP1 SIMILAR OPk) each arranged between each of the switch groups and the liquid crystal display, which supply LCD drive voltages (VO1 SIMILAR VO_k) of high drive current capacity to the liquid crystal display according to the drive source voltages supplied via the transistor switches. As no large current flows through each of the transistor switches, it is possible to reduce the transistor size. The LCD driver circuit of the invention is suited to the LCD requiring a number of display gradations and permits high density integration and cost reduction. <IMAGE>

IPC 1-7

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IPC 8 full level

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CPC (source: EP)

G09G 3/2011 (2013.01); **G09G 3/3688** (2013.01); **G09G 3/3614** (2013.01); **G09G 2310/027** (2013.01)

Citation (search report)

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- [A] GB 2103003 A 19830209 - SUWA SEIKOSHA KK [JP]

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