

Title (en)  
CIRCUIT ARRANGEMENT FOR VOLTAGE AND CURRENT REGULATION

Publication  
**EP 0497106 A3 19930331 (DE)**

Application  
**EP 92100310 A 19920110**

Priority  
DE 4102325 A 19910126

Abstract (en)  
[origin: EP0497106A2] 2.1 The circuit arrangement is to be constructed in such a manner that it has a minimum inherent current requirement and, in addition, has a very low minimum voltage drop between output and input. 2.2 Only a single circuit is provided for forming several supply and/or reference voltages for differential amplifiers. A regulating transistor operated as controllable resistance is arranged for current regulation and voltage regulation in only one line branch. 2.3 There are only a few circuits for generating supply and reference voltages within the circuit arrangement which drastically lowers the current consumption. In addition, the type and the operating mode of the regulating transistor is selected in such a manner that it can turn on with extremely low resistance. <IMAGE>

IPC 1-7  
**G05F 1/573**

IPC 8 full level  
**C08F 4/68** (2006.01); **G05F 1/573** (2006.01)

CPC (source: EP)  
**G05F 1/573** (2013.01)

Citation (search report)  
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