

Title (en)

DECISION-FEEDBACK EQUALIZER CAPABLE OF PRODUCING AN EQUALIZED SIGNAL AT A HIGH SPEED

Publication

**EP 0500041 A3 19921125 (EN)**

Application

**EP 92102674 A 19920218**

Priority

JP 4543991 A 19910219

Abstract (en)

[origin: EP0500041A2] In a decision-feedback equalizer for use in combination with a demodulator (13) to equalize a demodulated signal into an equalized signal through first and second transversal filters (15, 16), the demodulated signal and an input signal are filtered by the first and the second transversal filters into the first and the second filtered signals in accordance with a plurality of main controllable tap gains and a plurality of subsidiary controllable tap gains, respectively. The input signal supplied to the second transversal filter is controlled or selected by a supplying circuit (42). Specifically, the supplying circuit (42) produces as the input signal a first local signal obtained from the equalized signal when the demodulator is put in a synchronization state. The supplying circuit produces as the input signal a second local signal obtained from the demodulated signal when the demodulator is put out of the synchronization state. <IMAGE>

IPC 1-7

**H04L 25/03**

IPC 8 full level

**H03H 15/00** (2006.01); **H04B 3/14** (2006.01); **H04B 7/005** (2006.01); **H04L 25/03** (2006.01)

CPC (source: EP US)

**H04L 25/03057** (2013.01 - EP US); **H04L 2025/03541** (2013.01 - EP US); **H04L 2025/03573** (2013.01 - EP US)

Citation (search report)

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- [A] DE 3231768 A1 19840301 - STANDARD ELEKTRIK LORENZ AG [DE]
- [A] EP 0032522 A1 19810729 - ANT NACHRICHTENTECH [DE]
- [A] PATENT ABSTRACTS OF JAPAN vol. 009, no. 061 (E-303)(1784) & JP-A-59 200 533 ( NIPPON DENKI K.K. )

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**EP 0500041 A2 19920826; EP 0500041 A3 19921125; EP 0500041 B1 19960828**; DE 69213045 D1 19961002; DE 69213045 T2 19970206; JP 2611557 B2 19970521; JP H04264823 A 19920921; US 5285475 A 19940208

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**EP 92102674 A 19920218**; DE 69213045 T 19920218; JP 4543991 A 19910219; US 83805892 A 19920219