

Publication

EP 0501728 A3 19940803

Application

EP 92301535 A 19920224

Priority

JP 3279291 A 19910227

Abstract (en)

[origin: EP0501728A2] In a gradation corrector used in a television receiver, a clipping circuit (51) is connected to an output of a histogram memory (2) and a clipping level of the clipping circuit (51) is changed in accordance with an output of an S/N detecting circuit (50). Thereby, when the minimum value is detected, it is possible to prevent a large variation of the value to be detected which may be caused from noise or the like. Also, a circuit (52) for detecting a change in video scene and a recursive filter circuit composed of an adder (53) and a xK circuit (54) (and a two-input/one-output selector circuit (55)) are provided on the output side of a clipping circuit (51) and the coefficient value K of the xK circuit (54) (or the selector circuit (55)) is controlled in accordance with an output of the video scene change detecting circuit (52). Thereby, it is possible to make a smooth gradation correction which is not affected by noises or the like (or to make a gradation correction with rapid response which follows a change in video scene). <IMAGE>

IPC 1-7

H04N 5/14; **H04N 5/20**; **H04N 1/40**; **G06F 15/68**

IPC 8 full level

G06T 5/40 (2006.01); **H04N 5/14** (2006.01); **H04N 5/20** (2006.01); **H04N 5/21** (2006.01); **H04N 5/66** (2006.01)

CPC (source: EP KR US)

G06T 5/40 (2013.01 - EP KR US); **G06T 5/92** (2024.01 - EP KR US); **H04N 5/147** (2013.01 - EP US); **H04N 5/20** (2013.01 - EP US); **H04N 19/142** (2014.11 - EP US); **H04N 19/179** (2014.11 - EP US); **H04N 19/87** (2014.11 - EP US)

Citation (search report)

- [A] DE 3609049 A1 19861002 - CANON KK [JP]
- [ADP] PATENT ABSTRACTS OF JAPAN vol. 15, no. 334 (E - 1104) 26 August 1991 (1991-08-26)
- [A] PIZER ET AL.: "Adaptive Histogram Equalization and Its Variations", COMPUTER VISION GRAPHICS AND IMAGE PROCESSING, vol. 39, no. 3, September 1987 (1987-09-01), DULUTH, MA US, pages 355 - 368

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