

Title (en)

Microprocessor systems for electronic postage arrangements.

Title (de)

Mikroprozessorsysteme für Frankiermaschinenanordnungen.

Title (fr)

Systèmes à microprocesseur pour arrangements de machine à affranchir électronique.

Publication

EP 0513880 A2 19921119 (EN)

Application

EP 92114140 A 19830125

Priority

- EP 86116058 A 19830125
- US 34387782 A 19820129

Abstract (en)

A microprocessor system suitable for an electronic postage arrangement includes a microprocessor (10) having controllable address, data and control lines. First and second random access memories (20,21) are connected to the address, data and control lines. A printer (12) is coupled to the microprocessor. Feedback means (18) is provided to signal the printer setting to the microprocessor to enable updating of accounting data in the first and second random access memories. <IMAGE>

IPC 1-7

G07B 17/02

IPC 8 full level

G06F 12/16 (2006.01); **G07B 17/00** (2006.01); **G07B 17/04** (2006.01)

CPC (source: EP)

G07B 17/00362 (2013.01); **G07B 2017/00411** (2013.01)

Cited by

US6853990B1; EP0789333A2

Designated contracting state (EPC)

BE CH DE FR GB LI NL

DOCDB simple family (publication)

EP 0085385 A2 19830810; EP 0085385 A3 19841114; CA 1206619 A 19860624; DE 3382744 D1 19940519; DE 3382744 T2 19940901; DE 3382744 T3 20020905; DE 3382810 D1 19970213; DE 3382810 T2 19970522; DE 3382835 D1 20001130; DE 85385 T1 19851205; EP 0513880 A2 19921119; EP 0513880 A3 19930113; EP 0513880 B1 19970102; EP 0736846 A2 19961009; EP 0736846 A3 19961016; EP 0736846 B1 20001025; JP H0797417 B2 19951018; JP S58144989 A 19830829

DOCDB simple family (application)

EP 83100639 A 19830125; CA 419915 A 19830120; DE 3382744 T 19830125; DE 3382810 T 19830125; DE 3382835 T 19830125; DE 83100639 T 19830125; EP 92114140 A 19830125; EP 96110413 A 19830125; JP 1258583 A 19830128