

Title (en)

High-frequency power divider/combiner circuit.

Title (de)

Schaltung zum Aufteilen oder Zusammenführen von Hochfrequenzleistung.

Title (fr)

Circuit de combinaison ou de distribution de signaux de puissance à hautes fréquences.

Publication

**EP 0518310 B1 19950816 (DE)**

Application

**EP 92109809 A 19920611**

Priority

DE 4119631 A 19910614

Abstract (en)

[origin: EP0518310A1] In the case of the circuit for dividing radio-frequency power which is supplied from a summation gate (S) and passing it to a plurality of individual gates (E1, E2), and for combining radio-frequency power supplied from individual gates in a summation gate, having a plurality of transformation elements (L) connected between the summation gate and the individual gates, and having a load balancing impedance (Z), which acts between the individual gates, the individual gates are arranged at any desired physical distance from one another, and a complex impedance element (Z1), which acts as a load balancing impedance which is at a high level in terms of voltage, is arranged between said two individual gates, via connecting lines (L1, L2). <IMAGE>

IPC 1-7

**H01P 5/16**

IPC 8 full level

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CPC (source: EP)

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DOCDB simple family (publication)

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