Abstract (en)

[origin: EP0520400A2] A method for extracting, after data collection (DC) of a sampled analog signal (x(k)), a logical description of the analog signal by identification of the state (A, phi) of the signal, that is, the amplitude and phase of the signal, as well as an event (DELTA A, DELTA phi , k=h) which causes a change of a state at a sample k=h. The identification (SE) is performed with the aid of a truncated general Fourier series and an exponentially decreasing continuous value. Te identified parameters are supplied as input data to an expert system (ES) for forming the basis of a superordinate fault analysis together with binary data originating from other signals, the result thereof then being readable on a user interface (UI). <IMAGE>

IPC 1-7

G06F 15/20; G01R 31/00

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

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