Title (en)
Distributed crossbar switch architecture.

Title (de)
Architektur für verteilten Kreuzvermittlungsschalter.

Title (fr)
Architecture pour commutateur distribué à coordonnées.

Publication
EP 0531243 A1 19930310 (EN)

Application
EP 92480114 A 19920814

Priority
US 75604791 A 19910906

Abstract (en)
A switching array as described for connecting ones of a plurality of data sources (10, 12, 14) to ones of a plurality of data sinks (24, 26, 28).
The array includes source data busses which transmit from a connected source, data words, a connection mask and a connection designator.
The connection mask has bit positions that map to individual data sinks. A multiplexer/control circuit module (18, 20, 22) is associated with each
individual data sink for controlling the interconnection of the data sink to a source data bus. Each multiplexer/control circuit module is connected to
each source data bus and is responsive to an active state connection mask bit and a connection designator on a first source data bus, to establish a
connection between an associated sink and the first source data bus. This configuration enables the multiplexer/control circuit module to establish a
connection without reference to any other multiplexer/control circuit. <IMAGE>

IPC 1-7
G06F 15/16; H04L 12/00

IPC 8 full level
G06F 12/06 (2006.01); **G06F 15/173** (2006.01); **H04L 12/00** (2006.01)

CPC (source: EP US)
G06F 15/17375 (2013.01 - EP US); **H04L 12/00** (2013.01 - EP US); **H04L 49/101** (2013.01 - EP US)

Citation (search report)
- [A] EP 0276076 A2 19880727 - INMOS LTD [GB]
- [A] US 4982187 A 19910101 - GOLDSTEIN BARRY C [US], et al
- [A] WO 8909521 A1 19891005 - BELL TELEPHONE MFG [BE], et al

Cited by
GB2409370A

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0531243 A1 19930310; JP 3206126 B2 20010904; JP H05241947 A 19930921; US 5410300 A 19950425

DOCDB simple family (application)
EP 92480114 A 19920814; JP 22355192 A 19920731; US 10940093 A 19930819