

Title (en)

PROGRAMMABLE CONTROLLER AND SEQUENCE CONTROL METHOD

Publication

**EP 0539115 A3 19941109 (EN)**

Application

**EP 92309459 A 19921016**

Priority

JP 27744791 A 19911024

Abstract (en)

[origin: EP0539115A2] The present specification discloses a programmable controller which comprises bit operation memory for storing a mixed instruction inclusive of the mixture of bit operation instructions and word operation instructions in order of execution; a word operation memory for storing the word operation instructions in said mixed instruction in order of execution; a bit operation processor for outputting a start command for a word operation instruction when recognizing that word operation instruction read from said bit operation memory subsequently to the execution of the bit operation instruction; and a microprocessor for receiving said word operation start command to execute said word operation instruction stored in said word operation memory. <IMAGE>

IPC 1-7

**G06F 9/30**

IPC 8 full level

**G05B 19/05** (2006.01); **G06F 9/318** (2006.01); **G06F 9/38** (2006.01)

CPC (source: EP US)

**G05B 19/052** (2013.01 - EP US); **G06F 9/30018** (2013.01 - EP US); **G06F 9/30145** (2013.01 - EP US); **G06F 9/30181** (2013.01 - EP US);  
**G06F 9/3877** (2013.01 - EP US); **G06F 9/3879** (2013.01 - EP US); **G06F 9/3885** (2013.01 - EP US)

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Designated contracting state (EPC)

DE GB

DOCDB simple family (publication)

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JP 2848060 B2 19900120; JP H05127710 A 19930525; US 5504930 A 19960402

DOCDB simple family (application)

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