Title (en)

Expanded memory addressing scheme.

Title (de)

Erweitertes Speicheradressierungsschema.

Title (fr)

Schéma d'adressage de mémoire étendue.

Publication

EP 0543032 A1 19930526 (EN)

Application

EP 91119576 A 19911116

Priority

EP 91119576 A 19911116

Abstract (en)

A 31-bit addressing scheme is disclosed which is compatible with current 24-bit addressing schemes and allows an address space of 2GB to be created. The shared area of the operating system is divided into two separate areas (230, 260), one (230) located at the bottom of the address space and the other (260) at the top of the address space. Between the shared areas (230, 260), a continuous private area (240, 270) is available for use by the applications software. In one embodiment of the invention, the size of the bottom shared area (230) is limited to 1MB blocks so optimising the use of private area (230) by applications programs written for 24-bit addressing. <IMAGE>

IPC 1-7

G06F 12/02

IPC 8 full level

G06F 12/02 (2006.01)

CPC (source: EP US)

G06F 12/0292 (2013.01 - EP US)

Citation (search report)

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Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

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DOCDB simple family (application)

EP 91119576 A 19911116; US 97611692 A 19921113