

Title (en)

BICMOS BUS INTERFACE OUTPUT DRIVER COMPATIBLE WITH A MIXED VOLTAGE SYSTEM ENVIRONMENT

Publication

EP 0546724 A3 19931020 (EN)

Application

EP 92310827 A 19921126

Priority

US 80825391 A 19911213

Abstract (en)

[origin: EP0546724A2] A low voltage BiCMOS output driver (10) that is operable in a mixed voltage signal environment is provided which comprises a pull-up transistor (48) and blocking circuitry (54) operable to block current flow from the mixed voltage signal environment through the BiCMOS output driver (10) to its supply voltage source. <IMAGE>

IPC 1-7

H03K 19/0175; **H03K 19/003**; **H03K 19/013**

IPC 8 full level

H03K 19/003 (2006.01); **H03K 19/0175** (2006.01); **H03K 19/08** (2006.01); **H03K 19/094** (2006.01)

CPC (source: EP US)

H03K 19/00315 (2013.01 - EP US); **H03K 19/017518** (2013.01 - EP US); **H03K 19/09429** (2013.01 - EP US)

Citation (search report)

- [X] EP 0305676 A2 19890308 - IBM [US]
- [Y] US 5004936 A 19910402 - ANDRESEN BERNHARD H [US]
- [A] US 4301383 A 19811117 - TAYLOR DAVID L
- [A] EP 0414354 A1 19910227 - DIGITAL EQUIPMENT CORP [US]
- [Y] IEEE JOURNAL OF SOLID-STATE CIRCUITS vol. 23, no. 3, June 1988, NEW YORK US pages 816 - 819 PRIBYL ET AL 'cmos output buffer for megabit dram's'

Cited by

EP0709964A1; EP0660523A1; US5889415A; EP0630109A3; WO9829950A3; EP0722223B1

Designated contracting state (EPC)

DE FR GB IT NL

DOCDB simple family (publication)

EP 0546724 A2 19930616; **EP 0546724 A3 19931020**; **EP 0546724 B1 19970917**; DE 69222275 D1 19971023; DE 69222275 T2 19980122; JP 3424948 B2 20030707; JP H05259883 A 19931008; US 5276364 A 19940104

DOCDB simple family (application)

EP 92310827 A 19921126; DE 69222275 T 19921126; JP 33199192 A 19921211; US 80825391 A 19911213