

Title (en)

HIGH SPEED COLOR DISPLAY PROJECTION SYSTEM AND METHOD OF USING SAME

Publication

EP 0549717 A4 19950118 (EN)

Application

EP 91918571 A 19910918

Priority

- US 58650690 A 19900921
- US 61617890 A 19901119
- US 69053191 A 19910423

Abstract (en)

[origin: WO9205537A1] A high speed color display system (10) including a modulation circuit (11) electrically coupled between a high speed display device (16), such as an active matrix liquid crystal display panel, and a video output signal source (26, 14A) such as a video output card of a personal computer. The modulation circuit (11) including both a scaling circuit (35A) for eliminating or substantially reducing contouring caused by quantizing errors associated with approximations of digital pictures and a duty cycle scaling circuit (42A) for eliminating or substantially reducing repetitive beats in a displayed image. The display panel (16) is adapted for positioning on a conventional photographic projector for displaying large video images. Such a system (10) is modular and employs relatively inexpensive subassembly units such as a user selected television signal generating type device, such as a video cassette recorder (20), and a high speed display device, such as an active matrix display panel (16) adapted for positioning on a projector.

IPC 1-7

G09G 5/04

IPC 8 full level

G09G 5/00 (2006.01); **G09G 5/02** (2006.01); **G09G 5/04** (2006.01); **H04N 5/66** (2006.01)

CPC (source: EP)

G09G 5/02 (2013.01); **G09G 2300/023** (2013.01); **G09G 2320/0247** (2013.01); **G09G 2320/0276** (2013.01)

Citation (search report)

- [A] EP 0193728 A2 19860910 - ASCII CORP [JP], et al
- [A] EP 0387033 A2 19900912 - SHARP KK [JP]
- [A] WO 8607650 A1 19861231 - MUNDKUR KIRAN R, et al
- See references of WO 9205537A1

Designated contracting state (EPC)

AT BE CH DE DK ES FR GB IT LI LU NL SE

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