Title (en)
ELECTRONIC SYSTEM SIMULATION

Publication
EP 0556900 A3 19941109 (EN)

Application
EP 93200364 A 19930211

Priority
GB 9203507 A 19920219

Abstract (en)
[origin: EP0556900A2] A method of, and apparatus for, simulating an electronic system which comprises a sequence of interconnected functional blocks. The method comprises providing a high level macro model of each of the functional blocks, providing a low level description of each of the functional blocks in terms of a detailed circuit and/or physical layout and extracting parameters of each high level macro model from the low level description of the corresponding block by processing operations prior to simulation. The simulation comprises applying inputs signals to the system, computing the behaviour of each functional block in the sequence, including the interaction with its neighbours, using the extracted parameters of the high level macro models, and monitoring the signals at the output of the system to determine if the system behaves as anticipated. <IMAGE>

IPC 1-7
G06F 15/60

IPC 8 full level
G06F 11/26 (2006.01); **G06F 11/34** (2006.01); **G06F 17/50** (2006.01); **G06F 19/00** (2006.01)

CPC (source: EP US)
G06F 11/3457 (2013.01 - EP US); **G06F 30/367** (2020.01 - EP US)

Citation (search report)
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Designated contracting state (EPC)
DE FR GB

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