

Title (en)

Method and apparatus for buffering electrical signals.

Title (de)

Verfahren und Gerät zur Pufferung von elektrischen Signalen.

Title (fr)

Méthode et dispositif pour tamponner des signaux électriques.

Publication

EP 0558244 A1 19930901

Application

EP 93301263 A 19930222

Priority

US 84358092 A 19920228

Abstract (en)

A circuit and method for buffering a high output impedance voltage generator circuit to a low input impedance load circuit is described. Two emitter-follower stages are used with a load current feedback configuration so that the base to emitter voltages of all four transistors maintain a fixed relationship and therefore the output voltage presented to the load maintains a fixed relationship to the input voltage presented to the buffer amplifier circuit. <IMAGE>

IPC 1-7

H03F 3/50

IPC 8 full level

H03K 19/018 (2006.01); **H03F 3/343** (2006.01); **H03F 3/347** (2006.01); **H03F 3/50** (2006.01)

CPC (source: EP US)

H03F 3/50 (2013.01 - EP US)

Citation (search report)

- [X] EP 0394807 A2 19901031 - SGS THOMSON MICROELECTRONICS [IT]
- [X] EP 0355918 A1 19900228 - PHILIPS NV [NL]

Cited by

CN100411301C; US6861908B2; WO2004013959A1

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