

Title (en)

System and apparatus for coding and decoding a digital signal

Title (de)

System und Einrichtung zur Kodierung und Dekodierung eines digitalen Signals

Title (fr)

Système et dispositif de codage et décodage pour un signal digital

Publication

**EP 0561448 B1 19980902 (EN)**

Application

**EP 93200641 A 19930305**

Priority

NL 9200499 A 19920317

Abstract (en)

[origin: US5422672A] An encoder for coding a digital signal comprises an encoder input for receiving the digital signal; a first data processor, having an input which is coupled to the encoder input, for generating a first coded digital signal at a first output of the first data processor; a first reduction device coupled to the encoder input, for reducing the received digital signal; a second data processor, having an input which is coupled to the first reduction device, for generating a second coded digital signal at a first output of the second data processor; a first encoder memory having an input which is coupled to a second output of the first data processor, and having an output which is coupled to the input of the first data processor; a second encoder memory having an input which is coupled to a second output of the second data processor, and having an output which is coupled to the input of the second data processor; and a first encoder prediction device having a first side which is coupled to the second output of the second data processor, and a second side which is coupled to both the input of the first encoder memory and the input of the first data processor. A decoder for decoding a digital signal which is encoded as described above, comprises a first data reprocessing device for processing a coded digital signal; and a decoder memory having an input which is coupled to an output of the first data reprocessing device.

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