

Title (en)

ARRANGEMENT FOR RECORDING CAR DRIVING DATA WITH A TIME RESOLUTION ADAPTED TO THE SHAPE OF ANALOG MEASUREMENT SIGNALS

Title (de)

ANORDNUNG ZUR REGISTRIERUNG VON FAHRDATEN MIT EINER SICH DER SIGNALFORM VON ANALOGEN MESSSIGNALLEN ANPASSENDEN ZEITLICHEN AUFLÖSUNG

Title (fr)

DISPOSITIF POUR L'ENREGISTREMENT DE DONNEES CONCERNANT LE DEPLACEMENT D'UN VEHICULE AVEC UNE RESOLUTION TEMPORELLE ADAPTEE A LA FORME DE SIGNAUX DE MESURE ANALOGIQUES

Publication

EP 0566716 B1 19960327 (DE)

Application

EP 92922846 A 19921104

Priority

- DE 4136968 A 19911111
- EP 9202529 W 19921104

Abstract (en)

[origin: US5412570A] PCT No. PCT/EP92/02529 Sec. 371 Date Jun. 29, 1993 Sec. 102(e) Date Jun. 29, 1993 PCT Filed Nov. 4, 1992 PCT Pub. No. WO93/10510 PCT Pub. Date May 27, 1993. An apparatus for recording driving data which comprises a data gathering device, which further comprises a sensory measuring device, a control unit, an A/D converter, a plurality of parallel ring storage devices and a semiconductor storage device. Analog measurement signals, which are continuously detected by the sensory measuring device, for recording a vehicle movement, are continuously sensed by the control unit with two different frequencies after being digitized into digital measurement signals in the A/D converter. The digital signals are stored in the plurality of parallel ring storage devices with clock frequencies. Upon a detection of an accident, a trigger signal causes the control unit to stop storing the digital measurement signals in a first of the plurality of parallel ring storage devices with a lower clock frequency after a delay so that a storing of measurement data in the first of the plurality of parallel ring storage devices terminates one of after an after-running period and as a result of a stopping of the vehicle. The control unit interrupts a further storage of the digital measurement signals in a second of the plurality of parallel ring storage devices with a higher clock frequency at the occurrence of the trigger signal and causes the digital measurement signals to be stored in the semiconductor storage device. The semiconductor storage device is arranged in parallel with the second of the plurality of ring storage devices and has the higher clock frequency for the duration of the trigger signal.

IPC 1-7

G07C 5/08

IPC 8 full level

G01D 9/00 (2006.01); **G06F 17/40** (2006.01); **G07C 5/00** (2006.01); **G07C 5/08** (2006.01)

CPC (source: EP KR US)

G07C 5/08 (2013.01 - KR); **G07C 5/085** (2013.01 - EP US)

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