

Title (en)

Test circuit device for a fault current protective circuit breaker

Title (de)

Prüfkreisanordnung für einen Fehlerstromschutzschalter

Title (fr)

Dispositif à circuit de test pour un disjoncteur de protection à courants de défaut

Publication

**EP 0575899 B1 19960403 (DE)**

Application

**EP 93109744 A 19930618**

Priority

DE 4220569 A 19920624

Abstract (en)

[origin: EP0575899A1] A test circuit arrangement for a residual current device has a test circuit which is arranged between a phase conductor (L) and the return conductor (N) of a mains power supply and in which a current limiting resistor (19), the tripping relay (28) of the residual current device and a switch arrangement (11, 22) which can be operated by means of a test button (32, 33) are located which are connected in series with one another. This configuration prevents the current limiting resistor (19) burning out. The switch arrangement (11, 22) is constructed such that test circuit is closed only briefly during the operating process of the test button. In this case, the contact spring pair (14, 15) is closed first, and the initially closed contact spring pair (23, 24) is opened if the test button is pressed in further. <IMAGE>

IPC 1-7

**H01H 83/04**

IPC 8 full level

**H01H 83/04** (2006.01)

CPC (source: EP)

**H01H 83/04** (2013.01)

Designated contracting state (EPC)

AT BE CH DE ES FR GB IT LI

DOCDB simple family (publication)

**EP 0575899 A1 19931229; EP 0575899 B1 19960403**; AT E136397 T1 19960415; DE 4220569 A1 19940105; DE 59302090 D1 19960509;  
EP 0575898 A1 19931229; ES 2088192 T3 19960801

DOCDB simple family (application)

**EP 93109744 A 19930618**; AT 93109744 T 19930618; DE 4220569 A 19920624; DE 59302090 T 19930618; EP 93109743 A 19930618;  
ES 93109744 T 19930618