

Title (en)

Gate drive circuit for a MOS power transistor

Title (de)

Gate-Treiberschaltung für einen MOS-Leistungstransistor

Title (fr)

Circuit d'attaque de grille pour un transistor MOS de puissance

Publication

EP 0581580 B1 19980923 (EN)

Application

EP 93305948 A 19930728

Priority

US 92326692 A 19920731

Abstract (en)

[origin: EP0581580A2] An improved gate drive circuit for a MOS power transistor utilizes a dual gate drive circuit configuration. The first gate drive circuit operates to quickly turn on the MOS power transistor. After the MOS power transistor is turned on, the first gate drive circuit can be disabled. A second gate drive circuit is employed to provide continuous drive to the gate of the MOS power transistor to keep the MOS power transistor on.
<IMAGE>

IPC 1-7

H03K 17/04; **H03K 17/06**

IPC 8 full level

H03K 17/0412 (2006.01); **H03K 17/06** (2006.01); **H03K 17/687** (2006.01)

CPC (source: EP US)

H03K 17/04123 (2013.01 - EP US); **H03K 17/063** (2013.01 - EP US)

Cited by

EP0794619A3; EP1335492A3; FR2733094A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

EP 0581580 A2 19940202; **EP 0581580 A3 19941228**; **EP 0581580 B1 19980923**; DE 69321171 D1 19981029; DE 69321171 T2 19990408; JP H06177729 A 19940624; US 5359244 A 19941025

DOCDB simple family (application)

EP 93305948 A 19930728; DE 69321171 T 19930728; JP 19120693 A 19930802; US 92326692 A 19920731