

Title (en)  
PROGRAMMABLE INTERCONNECT STRUCTURE FOR LOGIC BLOCKS

Publication  
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Application  
**EP 92911404 A 19920506**

Priority  
US 69646291 A 19910506

Abstract (en)  
[origin: WO9220159A1] A structure for making programmable connections (X) between the input (I0?-I7?) and (O0?-O7?) output terminals of individual logic (20, 21) blocks in a logic device is disclosed. In one embodiment, each output terminal (O0?-O7?) is programmably connected to only one input terminal (I0?-I7?) of each logic block (20, 21). The same principle is followed in making connections between the input pins of the device and the input terminals of the logic blocks.

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IPC 8 full level  
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Citation (search report)

- No further relevant documents disclosed
- See references of WO 9220159A1

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DOCDB simple family (publication)  
**WO 9220159 A1 19921112**; DE 583361 T1 19950316; EP 0583361 A1 19940223; EP 0583361 A4 19940706; JP H06510403 A 19941117; US 5204556 A 19930420

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