

Publication

**EP 0584739 A3 19940504**

Application

**EP 93113348 A 19930820**

Priority

JP 22268692 A 19920821

Abstract (en)

[origin: EP0584739A2] A semiconductor integrated circuit is operative in a plurality of different modes. A plurality of select signals whose number corresponds to modes selected from a plurality of different modes are outputted. In response to the select signals, it is detected whether at least two operation modes are selected simultaneously. If so, a detection signal is outputted. In response to this detection signal, the operation of the semiconductor integrated circuit is stopped. Further, in response to the select signal, the semiconductor integrated circuit is activated in a mode by means of a predetermined select signal of these select signals. Further, in response to these select signals, the selected mode can be detected.  
<IMAGE>

IPC 1-7

**G06F 11/26**

IPC 8 full level

**G01R 31/28** (2006.01); **G01R 31/317** (2006.01); **G01R 31/3185** (2006.01); **H01L 21/82** (2006.01); **G06F 11/00** (2006.01)

CPC (source: EP KR US)

**G01R 31/31701** (2013.01 - EP US); **H01L 21/82** (2013.01 - KR); **G06F 11/0763** (2013.01 - EP US)

Citation (search report)

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- [A] EP 0283186 A2 19880921 - TOSHIBA KK [JP]
- [A] FR 2600453 A1 19871224 - MITSUBISHI ELECTRIC CORP [JP]

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