Title (en)

ARBITRARY WAVEFORM GENERATOR ARCHITECTURE.

Title (de)

ARCHITEKTUR FÜR EINEN GENERATOR ZUR ERZEUGUNG EINER WILLKÜRLICHEN WELLENFORM.

Title (fr)

ARCHITECTURE DE GENERATEUR DE FORMES D'ONDES ARBITRAIRES.

Publication

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Application

EP 93901022 A 19920623

Priority

- AU 9200305 W 19920623
- AU PK686191 A 19910625

Abstract (en)

[origin: WO9300737A1] A method of direct digital synthesis of linear frequency modulated wave forms comprising the steps of: at a given regular time, adding a fixed frequency increment word (1) to a frequency control value stored in a first register (4) to produce a lineally increasing frequency control word; adding the frequency control word stored in the first register (4) to a second register (8) to form a quadratically increasing phase word; converting the quadratically increasing phase word to an amplitude value using a lookup table stored in a memory means (9) to produce a lineally increasing frequency, and periodically resetting the frequency control word to produce a frequency sawtooth. Also a device for implementing the method being an arbitrary waveform generator comprising a pluraality of accumulators (4+5, 7+8), a memory means (9), a convertor means (10) and a clock means (6).

IPC 1-7

H03B 21/02; H03K 4/00

IPC 8 full level

G06F 1/02 (2006.01); **G01S 7/35** (2006.01); **G01S 13/34** (2006.01); **G06F 1/03** (2006.01); **H03B 28/00** (2006.01)

CPC (source: EP)

G06F 1/0335 (2013.01); G06F 2101/08 (2013.01)

Citation (search report)

- [XY] US 3842354 A 19741015 - DUNNE M
- [Y] PATENT ABSTRACTS OF JAPAN vol. 6, no. 92 (P - 119) 29 May 1982 (1982-05-29)
- See references of WO 9300737A1

Designated contracting state (EPC)

CH DE FR GB IT LI NL SE

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DOCDB simple family (application)

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