Title (en)
Compilation mechanism for a simulation model

Title (de)
Kompilierungsmechanismus für ein Simulationsmodell

Title (fr)
Mécanisme de compilation pour un modèle de simulation

Publication
EP 0592076 B1 20000809 (EN)

Application
EP 93305709 A 19930720

Priority
GB 9220085 A 19920923

Abstract (en)
[origin: EP0592076A2] A compilation mechanism generates a simulation model of a logic system comprising a plurality of design units connected together in a network. The compilation mechanism is controlled by a steering file comprising a set of statements. Each statement specifies whether a design unit is to be expanded as a sub-network, represented in terms of its behaviour specification, or both concurrently expanded as a sub-network and represented in terms of its behaviour specification. A CHECK command compares outputs from the sub-network and the behavioural simulation. A SUPPRESS command selectively inhibits checks on particular ports of the sub-network. <IMAGE>

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IPC 8 full level
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