

Title (en)

VIDEO PICTURE DISPLAY DEVICE AND METHOD FOR CONTROLLING VIDEO PICTURE DISPLAY

Publication

EP 0593012 A3 19970507 (EN)

Application

EP 93116496 A 19931012

Priority

JP 27403592 A 19921013

Abstract (en)

[origin: EP0593012A2] A video picture written in the bit map memory (4) is displayed in a redetermined area on the display screen (70) of the display device (7) within the computer main body (100). The masking memory (2) having a bit number equal to or less than the number of the pixels of the video picture to be inputted is provided separate from the bit map memory (4). When the pixel data of the video picture data has been inputted, the input pixel data is selectively written in the bit map memory (4) based on the contents of the masking memory (2). In displaying a compressed picture of the input video picture on the display screen, an oblique line generating algorithm is used. <IMAGE>

IPC 1-7

G09G 1/16; **G06F 3/14**

IPC 8 full level

G06F 3/153 (2006.01); **G09G 5/14** (2006.01); **G09G 5/36** (2006.01)

CPC (source: EP)

G09G 5/14 (2013.01); **G09G 2340/125** (2013.01)

Citation (search report)

- [XAY] GB 2215956 A 19890927 - BENCHMARK TECHNOLOGIES [GB]
- [XAY] US 4947257 A 19900807 - FERNANDEZ ANTONIO [US], et al
- [A] EP 0431845 A2 19910612 - RASTEROPS CORP [US]

Cited by

EP0870295A4; EP1139331A3; US7092585B2

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DE GB

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EP 0593012 A2 19940420; **EP 0593012 A3 19970507**; **EP 0593012 B1 20030305**; DE 69332733 D1 20030410; DE 69332733 T2 20040325; JP H06124189 A 19940506

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