

Title (en)
Push-pull matrix addressing.

Title (de)
Gegentaktadressierung einer Matrix.

Title (fr)
Adressage push-pull d'une matrice.

Publication
EP 0597226 A1 19940518 (EN)

Application
EP 93115480 A 19930924

Priority
US 97385492 A 19921109

Abstract (en)
A plurality of light emitting diodes (60) each diode including an anode (64) and a cathode (63), and divide into rows and columns. Rows and columns of conductors (62, 65) positioned to correspond, one each, with the rows and columns of diodes, and alternate columns of anodes and cathodes being reversed. Each row conductor has alternate cathodes and anodes connected thereto, and each column conductor with anodes connected thereto is connected to a column conductor with cathodes connected thereto so as to reduce the number of inputs by one half. A specific column of each pair is addressed by applying a positive or negative signal (b0) to each input. <IMAGE>

IPC 1-7
G09G 3/32

IPC 8 full level
G09G 3/32 (2006.01); **H01L 51/50** (2006.01)

CPC (source: EP)
G09G 3/3216 (2013.01); **G09G 3/3266** (2013.01); **G09G 3/3291** (2013.01); **G09G 3/32** (2013.01); **G09G 2300/0426** (2013.01); **G09G 2300/0439** (2013.01)

Citation (search report)
• [XA] US 3696393 A 19721003 - MCDONALD THOMAS W
• [A] FR 2207620 A5 19740614 - THOMSON CSF [FR]

Cited by
EP1170719A1; KR100370286B1; US7057590B2; CN1308905C; DE102004053387B4; US9905632B2; US8772795B2; US8575631B2; WO0205255A1; US8552440B2; US8975647B2; US6919868B2; US9516713B2; US8742405B2; US9349990B2; EP1274065A2; US7999463B2; US8436531B2; US8436532B2; US8487529B2; US8487530B2; US8912718B2

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