

Title (en)

METHOD FOR TEMPERATURE-COMPENSATING ZENER DIODES HAVING EITHER POSITIVE OR NEGATIVE TEMPERATURE COEFFICIENTS.

Title (de)

VERFAHREN ZUR TEMPERATURKOMPENSATION VON ZENERDIODEN MIT ENTWEDER POSITIVEN ODER NEGATIVEN TEMPERATURKOEFFIZIENTEN.

Title (fr)

PROCEDE DE COMPENSATION DE LA TEMPERATURE DE DIODES ZENER PRESENTANT DES COEFFICIENTS DE TEMPERATURE SOIT POSITIFS SOIT NEGATIFS.

Publication

EP 0600003 A4 19941102 (EN)

Application

EP 92918698 A 19920820

Priority

- US 9207039 W 19920820
- US 74808791 A 19910821

Abstract (en)

[origin: WO9304423A1] An auto-TC voltage reference (Vo) wherein an operational amplifier (20) receives at one input (22) the voltage of a Zener diode (24) and at its other input (38) receives a compensation signal from a feedback circuit (30A) comprising a transistor (Q4) and resistor network (40). When one of the resistors of the network is trimmed to give a nominal output voltage for the reference, the TC of the reference voltage will have been reduced to zero, or nearly so. The circuitry is capable of compensating Zener diodes of either positive or negative TC.

IPC 1-7

G05F 3/20; G05F 3/18

IPC 8 full level

G05F 1/56 (2006.01); **G05F 1/46** (2006.01); **G05F 1/567** (2006.01); **G05F 3/18** (2006.01)

CPC (source: EP)

G05F 1/463 (2013.01); **G05F 1/567** (2013.01); **G05F 3/18** (2013.01)

Citation (search report)

- [A] FR 2319932 A1 19770225 - NIPPON KOGAKU KK [JP]
- [A] GB 2080581 A 19820203 - RAYTHEON CO
- [A] US 3638049 A 19720125 - BOM JOHANNES GERARDUS WOUTERUS
- [A] HOLLOWAY ET AL.: "Circuit Techniques for Achieving High-Speed Resolution A/D Conversion", ISSCC79, 15 February 1979 (1979-02-15), PEALE BALLROOM HOLIDAY INN, pages 136 - 137
- See references of WO 9304423A1

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

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