

Title (en)
UNIVERSAL INTERCONNECT MATRIX ARRAY.

Publication
EP 0609264 A4 19971001 (EN)

Application
EP 92920784 A 19920923

Priority
• US 9208115 W 19920923
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Abstract (en)
[origin: WO9306559A1] A universal interconnect matrix area array (605) comprised of a first set of conductive leads (608-1 through 608-J) formed in a first direction, a second set of conductive leads (609-1 through 609-K) formed in a second direction, the second direction being not parallel to the first direction, and structure for electrically interconnecting selected ones of the conductive leads in the first set of conductive leads (608-1 through 608-J) to one or more of the conductive leads in the second set of conductive leads (609-1 through 609-K). Input/output pads (607-1,1 through 607-M,N) are formed and connected to selected ones of the first set of conductive leads (608-1 through 608-J) and second set of conductive leads (609-1 through 609-K). Selected ones of the conductive leads are segmented thereby to allow any input/output pad to be connected to one or more of the other input/output pads (607-1,1 through 607-M,N) without removing from use any input/output pads (607-1,1 through 607-M,N), not intended to be so connected.

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G06F 15/60; **H04Q 9/00**; **H03K 17/693**

IPC 8 full level
H01L 23/525 (2006.01); **H03K 17/00** (2006.01); **H03K 17/693** (2006.01)

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C-Set (source: EP)
H01L 2924/0002 + **H01L 2924/00**

Citation (search report)
• [XA] EP 0335281 A2 19891004 - KALETO AG [CH]
• [PX] EP 0481703 A2 19920422 - APTIX CORP [US]
• [E] EP 0518701 A2 19921216 - APTIX CORP [US]
• [PX] GUO R ET AL: "A 1024 PIN UNIVERSAL INTERCONNECT ARRAY WITH ROUTING ARCHITECTURE", PROCEEDINGS OF THE CUSTOM INTEGRATED CIRCUITS CONFERENCE, BOSTON, MAY 3 - 6, 1992, no. CONF. 14, 3 May 1992 (1992-05-03), INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, pages 4.5.1 - 4.5.4, XP000340845
• See references of WO 9306559A1

Designated contracting state (EPC)
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