

Title (en)
Video subsystem for a computer system.

Title (de)
Videosubsystem für ein Rechnersystem.

Title (fr)
Sous-système vidéo pour un système d'ordinateur.

Publication
EP 0612053 A1 19940824 (EN)

Application
EP 94301116 A 19940216

Priority
US 1779493 A 19930216

Abstract (en)
A video subsystem for a computer system comprising a video adapter and a video monitor electrically communicating over a display cable having at least one monitor identification line . The three system components are designed such that after the video adapter reads the monitor identification from the monitor identification lines, the adapter and monitor reuse the monitor identification lines as a bidirectional serial link, over which large amounts and types of data may be transferred. <IMAGE>

IPC 1-7
G09G 1/16; **G06F 3/14**

IPC 8 full level
G06F 13/38 (2006.01); **G09G 1/16** (2006.01); **G09G 5/00** (2006.01)

CPC (source: EP KR)
G09G 5/006 (2013.01 - EP KR); **G09G 2370/04** (2013.01 - EP); **G09G 2370/042** (2013.01 - EP KR)

Citation (search report)

- [XY] EP 0456923 A1 19911121 - IBM [US]
- [A] DE 4025295 A1 19910214 - APPLE COMPUTER [US]
- [Y] "Self-identification Protocol Initialization", IBM TECHNICAL DISCLOSURE BULLETIN., vol. 33, no. 10A, March 1991 (1991-03-01), NEW YORK US, pages 406 - 407
- [A] PATENT ABSTRACTS OF JAPAN vol. 11, no. 83 (E - 489) 13 March 1987 (1987-03-13)

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Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
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DOCDB simple family (application)
EP 94301116 A 19940216; JP 705594 A 19940126; KR 19940002530 A 19940214