

Title (en)  
DIGITAL DEMODULATOR.

Title (de)  
DIGITALER DEMODULATOR.

Title (fr)  
DEMODULATEUR NUMERIQUE.

Publication  
**EP 0613595 A4 19950118 (EN)**

Application  
**EP 92924443 A 19921117**

Priority  

- US 9209857 W 19921117
- US 79415091 A 19911120
- US 87584892 A 19920429
- US 90036792 A 19920618

Abstract (en)  
[origin: WO9310596A1] A digital demodulator for frequency modulated, pulse-width modulated and other temporally modulated signals uses a sign detector (14) to determine zero-crossings of the modulated signal. The periods between zero-crossings are determined by at least one gate circuit (16, 18) and at least one counter (22, 24) which counts clock pulses from a high-speed clock (20) gated by the gate circuit in response to zero-crossings. The period information is converted to a demodulated signal by a numerical processor (12), or a signal processor (12') or a digital-to-analog converter (12'').

IPC 1-7  
**H03D 3/00**

IPC 8 full level  
**H03M 1/86** (2006.01); **H03D 3/00** (2006.01); **H03D 3/04** (2006.01); **H03K 9/06** (2006.01); **H03K 9/08** (2006.01); **H04L 27/14** (2006.01)

CPC (source: EP)  
**H03D 3/04** (2013.01); **H03K 9/06** (2013.01); **H03K 9/08** (2013.01)

Citation (search report)  

- [XA] US 4992748 A 19910212 - GARD MICHAEL F [US]
- [XA] US 3548328 A 19701215 - BREIKSS IVARS P
- [A] EP 0099142 A2 19840125 - PHILIPS PATENTVERWALTUNG [DE], et al
- See references of WO 9310596A1

Designated contracting state (EPC)  
DE FR GB IE NL

DOCDB simple family (publication)  
**WO 9310596 A1 19930527**; CA 2124114 A1 19930527; CA 2124114 C 19990330; EP 0613595 A1 19940907; EP 0613595 A4 19950118; JP H08500942 A 19960130; KR 100363288 B1 20030211

DOCDB simple family (application)  
**US 9209857 W 19921117**; CA 2124114 A 19921117; EP 92924443 A 19921117; JP 50944893 A 19921117; KR 19940701703 A 19940520