

Title (en)

Methods and apparatus for accelerating windows in graphics systems.

Title (de)

Verfahren und Einrichtung zur Beschleunigung von Bildfenstern in graphischen Systemen.

Title (fr)

Méthode et dispositif pour l'accélération de fenêtres dans des systèmes graphiques.

Publication

**EP 0617401 A3 19950426 (EN)**

Application

**EP 94201809 A 19900727**

Priority

- EP 90308257 A 19900727
- US 38751089 A 19890728

Abstract (en)

[origin: EP0410783A2] Graphics window systems which utilize graphics pipelines and graphics pipeline bypass buses. Hardware solutions for window relative rendering of graphics primitives, block moving of graphics primitives, transfer of large data blocks, and elimination of pipeline flushing are disclosed. The hardware implementations provided in accordance with the invention are interfaced along the pipeline bypass bus, thereby eliminating gross overhead processor time for the graphics pipeline and reducing pipeline latency. Methods and apparatus provided in accordance with the invention exhibit significant pipeline efficiency and reductions in time to render graphics primitives to the screen system.

IPC 1-7

**G09G 5/14**

IPC 8 full level

**G09G 5/14** (2006.01); **G09G 5/393** (2006.01)

CPC (source: EP)

**G09G 5/14** (2013.01); **G09G 5/393** (2013.01); **G09G 2360/121** (2013.01)

Citation (search report)

- [A] US 4829294 A 19890509 - IWAMI HIDEFUMI [JP], et al
- [A] EP 0303290 A2 19890215 - DIGITAL EQUIPMENT CORP [US]

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**EP 0410783 A2 19910130; EP 0410783 A3 19910710; EP 0410783 B1 19960828**; DE 69028259 D1 19961002; DE 69028259 T2 19970123; DE 69033158 D1 19990715; DE 69033158 T2 19991014; DE 69033283 D1 19991014; DE 69033283 T2 19991230; EP 0617400 A2 19940928; EP 0617400 A3 19950426; EP 0617400 B1 19990908; EP 0617401 A2 19940928; EP 0617401 A3 19950426; EP 0617402 A2 19940928; EP 0617402 A3 19950426; EP 0617402 B1 19990609

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