

Title (en)  
Display system.

Title (de)  
Anzeigesystem.

Title (fr)  
Système d'affichage.

Publication  
**EP 0618561 A2 19941005 (EN)**

Application  
**EP 94200912 A 19900514**

Priority  
EP 90305158 A 19900514

Abstract (en)  
A display system is described, comprising a display device (88) for generating a visual output in response to a plurality of data signals defining the data to be displayed, a display adapter circuit (92) for generating the data signals in a form specified by control data identifying the display device (88), an output port (94) for connecting the data signals from the display adapter circuit to the display device (88) and for connecting the control data from the display device (88) to the display adapter circuit (92), characterised in that the display system further comprises a non-volatile memory (9) located in the display device (88) for storing the control data in the form of a plurality of control codes, and communication logic (95) for communicating a control code between the memory and the output port (94) in response to a command signal (21) generated by the display adapter circuit (92). The communication logic comprises a serial data link (3) for communicating the control code between the display device and the output port, device logic (97) located in the display device for communicating the control code between the memory and the serial data link, and adapter logic (96) located in the display adapter circuit for communicating the control code between the serial data link and the display adapter circuit. The control data, such as the signal timing requirements of any new display device, can be stored in the form of digital control codes held within the memory of the display device, the display system programming does not require updating every time a different display device is connected to the output port. Instead, the display adaptor can read the new timing requirements from the memory of the new display device for the purpose of generating video and sync signals for correctly driving the new display device. Furthermore, where the display system comprises a multiple mode display device, the display adapter circuit can use the serial link to configure the display device to operate in a desired display mode.

IPC 1-7  
**G09G 1/16**; **G06F 3/14**

IPC 8 full level  
**G06F 3/14** (2006.01); **G06F 3/153** (2006.01); **G09G 1/16** (2006.01); **G09G 5/00** (2006.01); **G09G 5/12** (2006.01); **G09G 5/18** (2006.01)

CPC (source: EP)  
**G09G 5/006** (2013.01); **G09G 2370/04** (2013.01); **G09G 2370/042** (2013.01)

Designated contracting state (EPC)  
CH DE ES FR GB IT LI NL SE

DOCDB simple family (publication)  
**EP 0618561 A2 19941005**; **EP 0618561 A3 19941102**; **EP 0618561 B1 19960306**; DE 69013674 D1 19941201; DE 69013674 T2 19950504; DE 69025776 D1 19960411; DE 69025776 T2 19960926; EP 0456923 A1 19911121; EP 0456923 B1 19941026; ES 2084525 T3 19960501; JP 2635837 B2 19970730; JP 2815339 B2 19981027; JP H07302068 A 19951114; JP H09128182 A 19970516

DOCDB simple family (application)  
**EP 94200912 A 19900514**; DE 69013674 T 19900514; DE 69025776 T 19900514; EP 90305158 A 19900514; ES 94200912 T 19900514; JP 21227796 A 19960812; JP 8024691 A 19910412