

Title (en)  
Integrated analog-digital boundary interface.

Title (de)  
Integrierte analog-digitale Grenzschnittstelle.

Title (fr)  
Interface mitoyenne analogique-numérique intégrée.

Publication  
**EP 0619559 A3 19951025 (EN)**

Application  
**EP 94302338 A 19940331**

Priority  
US 4272293 A 19930405

Abstract (en)  
[origin: EP0619559A2] Invention relates to an integrated analog-digital boundary interface for time multiplexed communication systems. The analog-digital boundary interface consists of an integrated AFE circuit (17) in which functioning of A/D and D/A converters are combined. The solution according to the invention is efficient and advantageous from the point of view of interference control and complexity. <IMAGE>

IPC 1-7  
**G06J 1/00**

IPC 8 full level  
**G06J 1/00** (2006.01)

CPC (source: EP)  
**G06J 1/00** (2013.01)

Citation (search report)

- [X] GB 1585610 A 19810311 - SIEMENS AG
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- [A] EP 0482745 A2 19920429 - ADVANCED MICRO DEVICES INC [US]
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- [X] US 4160243 A 19790703 - MORIYA TAKAO, et al
- [A] HASPELAGH ET AL: "A Four-Channel Digital Signal Processor in 1.2um CMOS with On-Chip D/A and A/D Conversion Serving Four Speech Channels in a New-Generation Subscriber Line Circuit", IEEE JOURNAL OF SOLID-STATE CIRCUITS, vol. 26, no. 7, July 1992 (1992-07-01), NEW YORK, US, pages 1038 - 1045, XP000237046
- [XA] SCHWARZ ET AL: "An Integrated Multichannel Approach to PCM", IEEE JOURNAL OF SOLID-STATE CIRCUITS, vol. SC-14, no. 6, December 1979 (1979-12-01), pages 954 - 959, XP001404429
- [A] LAKSHMIKUMAR ET AL: "A Baseband Coded for Digital Cellular Telephony", IEEE JOURNAL OF SOLID-STATE CIRCUITS, vol. 26, no. 12, December 1991 (1991-12-01), NEW YORK, US, pages 1951 - 1957, XP000272856

Designated contracting state (EPC)  
DE FR GB

DOCDB simple family (publication)  
**EP 0619559 A2 19941012; EP 0619559 A3 19951025**

DOCDB simple family (application)  
**EP 94302338 A 19940331**