

Title (en)  
Temperature-compensated voltage regulator

Title (de)  
Temperaturkompensierter Spannungsregler

Title (fr)  
Diviseur de tension avec compensation de la température

Publication  
**EP 0620514 B1 20000301 (EN)**

Application  
**EP 94200862 A 19940330**

Priority  
US 4341893 A 19930406

Abstract (en)  
[origin: EP0620514A2] A temperature-compensated voltage regulator includes a field effect transistor voltage buffer (30) which receives a high-voltage (HVIN) input and provides a low-voltage output (VREG), and a voltage generator having a series connection of a zener diode (32) and at least one p-n junction diode (34, 36, 38) for generating a reference voltage. The voltage generator is coupled between the low-voltage output (VREG) of the voltage buffer (30) and the input of a current mirror (40, 42), with the output of the current mirror being coupled to the gate electrode of the field effect transistor (30) in the voltage buffer. Additionally, the output of the current mirror (40, 42) is coupled to the low-voltage output (VREG) of the voltage buffer by a resistor (RL). <IMAGE>

IPC 1-7  
**G05F 3/26**

IPC 8 full level  
**G05F 3/18** (2006.01); **G05F 3/26** (2006.01)

CPC (source: EP US)  
**G05F 3/267** (2013.01 - EP US); **Y10S 323/907** (2013.01 - EP)

Cited by  
EP1253498A1; CN105388950A; US9280169B2; US6700361B2; WO2012003871A1

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