

Title (en)  
Current source arrangement.

Title (de)  
Stromquellenanordnung.

Title (fr)  
Dispositif source de courant.

Publication  
**EP 0623865 A3 19950111 (DE)**

Application  
**EP 94106767 A 19940429**

Priority  
DE 4315299 A 19930507

Abstract (en)  
[origin: DE4315299C1] The current source device has a current source supplying a pair of exclusive=OR parallel transistors (S1,S2). The current source comprises a regulated cascade stage (P1,P2,P3,N1). The first transistor (P1) is controlled by a bias voltage (B1) and the control input of the second transistor (P2) is coupled to the output of a regulating transistor (P3).The control input of the latter is coupled to a junction (K1) between the first two transistors. The cascade transistors and the regulating transistors are of p-channel type, the parallel transistors being of n-channel type.

IPC 1-7  
**G05F 3/24**

IPC 8 full level  
**G05F 3/24** (2006.01)

CPC (source: EP)  
**G05F 3/24** (2013.01)

Citation (search report)

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Designated contracting state (EPC)  
DE FR GB IT

DOCDB simple family (publication)  
**DE 4315299 C1 19940623**; DE 59404502 D1 19971211; EP 0623865 A2 19941109; EP 0623865 A3 19950111; EP 0623865 B1 19971105

DOCDB simple family (application)  
**DE 4315299 A 19930507**; DE 59404502 T 19940429; EP 94106767 A 19940429