

Title (en)  
Artificial neuron and method of using same.

Title (de)  
Künstliches Neuron.

Title (fr)  
Neurone artificiel.

Publication  
**EP 0629969 A1 19941221 (EN)**

Application  
**EP 94304233 A 19940613**

Priority  
US 7660293 A 19930614

Abstract (en)  
An artificial neuron, which may be implemented either in hardware or software, has only one significant processing element in the form of a multiplier (22, FIG. 4). Inputs are first fed through gating functions to produce gated inputs. These gated inputs are then multiplied together to produce a product which is multiplied by a weight to produce the neuron output. <IMAGE>

IPC 1-7  
**G06F 15/80**; **G06F 7/552**

IPC 8 full level  
**G06F 15/18** (2006.01); **G06F 7/552** (2006.01); **G06N 3/063** (2006.01); **G06N 3/08** (2006.01)

CPC (source: EP KR US)  
**G06F 7/552** (2013.01 - EP KR US); **G06N 3/063** (2013.01 - EP KR US); **G06F 2207/5523** (2013.01 - EP KR US)

Citation (search report)  
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• [A] WO 8707053 A1 19871119 - GEC AVIONICS [GB], et al  
• [A] EP 0218971 A2 19870422 - SIEMENS AG [DE]  
• [A] ZHANG: "Recursive reduction in finite ring computations", PROCEEDINGS OF THE TWENTY-THIRD ASILOMAR CONFERENCE ON SIGNALS, SYSTEMS & COMPUTERS, 30 October 1989 (1989-10-30), PACIFIC GROVE , USA, pages 854 - 857, XP000217517  
• [A] HABIB: "A digital neuron-type processor and its VLSI design", IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS, vol. 36, no. 5, May 1989 (1989-05-01), NEW YORK US, pages 739 - 746, XP000036104

Designated contracting state (EPC)  
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**US 6151594 A 20001121**; CA 2125244 A1 19941215; CN 1107598 A 19950830; EP 0629969 A1 19941221; JP H0713950 A 19950117; KR 950001520 A 19950103; MX 9404367 A 19950131; TW 240304 B 19950211; US 5390136 A 19950214

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**US 29423594 A 19940822**; CA 2125244 A 19940606; CN 94106565 A 19940610; EP 94304233 A 19940613; JP 15165494 A 19940609; KR 19940013277 A 19940610; MX 9404367 A 19940609; TW 83104209 A 19940510; US 7660293 A 19930614