

Title (en)

Process and device composite simulation system and simulation method.

Title (de)

Zusammengesetztes Simulationssystem eines Prozesses und Einrichtung und Verfahren zur Simulation.

Title (fr)

Système composite de simulation de procédé et de dispositif et méthode de simulation.

Publication

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Application

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Priority

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Abstract (en)

A unified process and device composite simulation system unitarily performs simulation for a process designing and a device designing of a semiconductor device. The system includes a process simulator performing a process simulation for one of a pair of regions of the semiconductor device, which regions are symmetric in configuration and structure about a center line extending perpendicularly to a direction to progress the simulation, and generating a first simulation data. The system also includes a mirror reversal processing portion for performing a mirror reversing process for establishing a mirror data of the first simulation data symmetric about the center line on the basis of the structure data of the semiconductor and connecting the mirror data to the first simulation data at the center line in order to generate a second data equivalent to a process simulation data obtained through the process simulation for overall region of the semiconductor device, and a device simulator for performing a device simulation with respect to the second data on the basis of a predetermined analysis condition. <IMAGE>

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IPC 8 full level

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