

Title (en)  
Circuit for converting a stereo signal.

Title (de)  
Schaltungsanordnung zum Umwandeln eines Stereosignals.

Title (fr)  
Circuit de couversion d'un signal stéréo.

Publication  
**EP 0639038 A3 19950927 (DE)**

Application  
**EP 94202275 A 19940808**

Priority  
DE 4326811 A 19930810

Abstract (en)  
[origin: EP0639038A2] A circuit arrangement for converting a stereo signal comprising a centre signal and a side signal into one output signal each for two audio signal channels with a calibration arrangement for minimising crosstalk between the output signals (at 11, 12) is described, which - comprises a (first) limiter circuit (16) which can be supplied with a first one of the output signals (from 11) and which can convert this into an at least almost rectangular (first) amplitude-limited signal of the same frequency, - a (first) mixer circuit (19) for obtaining a (first) DC signal by multiplicatively combining the (first) amplitude-limited signal with a second one of the output signals (at 12), - a (first) control circuit (22) for obtaining a (first) control signal from the (first) DC signal, - a setting circuit (8) for influencing the amplitudes of the centre signal and/or the side signal with the aid of the (first) control signal (from 24), - a (first) storage device for storing the (first) control signal (from 24) and - a (first) switch-over device (25), by means of which the (first) control signal (from 24) can be supplied both to the (first) storage device for storing and to the setting circuit (8) in a first operating mode of the circuit arrangement and the (first) control signal stored in the (first) storage device can be supplied to the setting circuit (8) in a second operating mode of the circuit arrangement. By means of this circuit arrangement according to the invention, a simple, even automatic calibration of the crosstalk is possible. <IMAGE>

IPC 1-7  
**H04S 1/00**

IPC 8 full level  
**H04S 3/02** (2006.01); **H04B 1/16** (2006.01); **H04S 1/00** (2006.01); **H04S 5/02** (2006.01)

CPC (source: EP US)  
**H04S 1/007** (2013.01 - EP US)

Citation (search report)  
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• [A] PATENT ABSTRACTS OF JAPAN vol. 7, no. 76 (E - 167) 30 March 1983 (1983-03-30)

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DE FR GB IT

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