

Title (en)

CMOS on-board voltage regulator.

Title (de)

Im Fahrzeug eingebauter CMOS Spannungsregler.

Title (fr)

Régulateur de tension CMOS embarqué.

Publication

EP 0651309 A3 19970716 (EN)

Application

EP 94114202 A 19940909

Priority

US 14203593 A 19931028

Abstract (en)

[origin: EP0651309A2] A voltage regulator for regulating the voltage at a predetermined node connected to the core of a semiconductor device comprising reference voltage means for generating a reference voltage, an op-amp having said reference voltage as its reference input and the voltage at said predetermined node as its another input for adjusting the node voltage to match said reference voltage, series dropping means for supplying current load to the core, and filter capacitor coupled to said series dropping means for supplying transient current to said predetermined node. <IMAGE>

IPC 1-7

G05F 1/56

IPC 8 full level

G05F 1/46 (2006.01); **G05F 1/56** (2006.01)

CPC (source: EP)

G05F 1/465 (2013.01); **G05F 1/56** (2013.01)

Citation (search report)

- [XY] US 5132555 A 19920721 - TAKAHASHI TOSHIRO [JP]
- [XA] EP 0539593 A1 19930505 - FUJITSU LTD [JP]
- [XA] US 5083043 A 19920121 - YOSHIDA MAKOTO [JP]
- [A] US 4806844 A 19890221 - CLAYDON GLENN S [US], et al
- [Y] US 5168209 A 19921201 - THIEL V FRANK L [US]

Cited by

EP0749059A3; FR2800936A1

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

EP 0651309 A2 19950503; EP 0651309 A3 19970716; JP H07182053 A 19950721

DOCDB simple family (application)

EP 94114202 A 19940909; JP 25816794 A 19941024