

Title (en)  
Operational amplifier.

Title (de)  
Operationsverstärker.

Title (fr)  
Amplificateur opérationnel.

Publication  
**EP 0651500 A1 19950503 (EN)**

Application  
**EP 94306622 A 19940909**

Priority  
US 14654393 A 19931101

Abstract (en)  
An op-amp comprising a single gain stage amplifier cascaded with a buffer and an output stage. The buffer comprises an amplifier which isolates the gain stage from the output stage to prevent loading of the gain stage and create a more linear op-amp. For frequency compensation, the op-amp utilizes MOSFETs connected in a reversed biased configuration as load compensation capacitors. This technique reduces the non-linear effects of MOSFET gate capacitors utilized in conventional Miller compensation schemes and allows for digital fabrication technology of low distortion, low power supply operational amplifier design. <IMAGE>

IPC 1-7  
**H03F 1/08**

IPC 8 full level  
**H03F 3/45** (2006.01); **H03F 1/08** (2006.01); **H03F 1/32** (2006.01)

CPC (source: EP US)  
**H03F 1/086** (2013.01 - EP US)

Citation (search report)  
• [X] EP 0479119 A2 19920408 - MOTOROLA INC [US]  
• [A] R. GOMEZ ET AL: "A 50-MHZ CMOS VARIABLE GAIN AMPLIFIER FOR MAGNETIC DATA STORAGE SYSTEMS", IEEE JOURNAL OF SOLID-STATE CIRCUITS., vol. 27, no. 6, June 1992 (1992-06-01), NEW YORK US, pages 935 - 939, XP000306395  
• [A] J. FICHEL ET AL: "DESIGN AND APPLICATIONS OF TUNABLE ANALOG BICMOS CIRCUITS", IEEE JOURNAL OF SOLID-STATE CIRCUITS., vol. 27, no. 7, July 1992 (1992-07-01), NEW YORK US, pages 1101 - 1104, XP000304449

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Designated contracting state (EPC)  
BE DE DK ES FR GB GR IE IT LU NL PT SE

DOCDB simple family (publication)  
**EP 0651500 A1 19950503**; JP H07193436 A 19950728; US 5410273 A 19950425

DOCDB simple family (application)  
**EP 94306622 A 19940909**; JP 26645894 A 19941031; US 14654393 A 19931101