

Title (en)
Input/output interface circuit for digital and/or analog signals

Title (de)
Eingangs-/Ausgangsschnittstellenschaltung für analoge und digitale Signale

Title (fr)
Circuit d'interface entrée/sortie pour des signaux analogues et numériques

Publication
EP 0651503 B1 19980722 (EN)

Application
EP 93830436 A 19931029

Priority
EP 93830436 A 19931029

Abstract (en)
[origin: EP0651503A1] The invention relates to an integrated input/output interface for low and/or high voltage signals of the digital and/or analog type. It comprises essentially a power amplification circuit block (2) having at least one low voltage input (A) and at least one high voltage output (B), and a second amplification circuit block (3) including active power components and having a high voltage input connected to said high voltage output (B) and at least one low voltage output (D). A conventional circuit block (4) prevents a high voltage signal being input to (B) from propagating through the block (2), so that it only affects the block (3). This interface is implemented in mixed high voltage BCD technology. <IMAGE>

IPC 1-7
H03F 3/62; G06J 1/00

IPC 8 full level
H03K 19/018 (2006.01); **G06J 1/00** (2006.01); **H03F 3/62** (2006.01); **H03F 3/68** (2006.01)

CPC (source: EP US)
G06J 1/00 (2013.01 - EP US); **H03F 3/62** (2013.01 - EP US)

Designated contracting state (EPC)
DE FR GB IT

DOCDB simple family (publication)
EP 0651503 A1 19950503; EP 0651503 B1 19980722; DE 69319910 D1 19980827; DE 69319910 T2 19981210; JP H07263982 A 19951013;
US 5565806 A 19961015

DOCDB simple family (application)
EP 93830436 A 19931029; DE 69319910 T 19931029; JP 24959994 A 19941014; US 26259094 A 19940620