

Title (en)  
Interference detector

Title (de)  
Schaltung zur Detektion von Interferenz

Title (fr)  
Circuit de détection de l'interférence

Publication  
**EP 0651519 B1 20000426 (EN)**

Application  
**EP 95101367 A 19901207**

Priority  
• EP 90123570 A 19901207  
• JP 2617890 A 19900207  
• JP 2617990 A 19900207

Abstract (en)  
[origin: EP0651519A2] An interference detector for an interference reducing device is provided which comprises input means 103 for receiving a digitally modulated signal; a voltage-controlled oscillator 122 for generating an oscillation frequency output; a mixer 117 for mixing the digital modulated signal with said oscillation frequency output; a multiplexer 118 for dividing the output of said mixer into a plurality of signal frequency band parts; A/D converters 19, 20 for converting said divided signals into digital voltage outputs; a CPU 124 for detecting a narrow band interference carrier included in said received digitally modulated signal by the use of said converted voltage outputs; and a D/A converter 21 for converting the output of said CPU to an analog output to control said oscillation frequency output of said voltage-controlled oscillator with the CPU output.  
<IMAGE>

IPC 1-7  
**H04B 1/10**

IPC 8 full level  
**H03H 11/12** (2006.01); **H04B 1/10** (2006.01); **H04B 1/12** (2006.01); **H04B 7/212** (2006.01)

CPC (source: EP)  
**H04B 1/1027** (2013.01); **H04B 2001/1072** (2013.01)

Cited by  
FR2753592A1; CN111510119A; GB2567159A; GB2567159B; US10992329B2; US11621735B2; US11804867B2

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DE FR

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