

Title (en)

Gate electrode formed in trench and method of making the same.

Title (de)

Im Graben gebildete Gateelektrode und Verfahren zur Herstellung.

Title (fr)

Electrode de grille formée dans une tranchée et sa méthode de fabrication.

Publication

EP 0655786 A3 19960228 (EN)

Application

EP 94118437 A 19941123

Priority

JP 32988893 A 19931130

Abstract (en)

[origin: EP0655786A2] In a method of making a semiconductor apparatus which is capable of controlling fluctuations of the effective length of the conduction area which occurs at making, and a semiconductor apparatus using this method, the edge of the first and/or second electric conduction film is formed by self adjustment to the trench, and the position of the edge of impurity diffusion zone is formed by self adjustment to the edge of electric conduction film. Thus, the fluctuation of effective length of the conduction area can be avoided. Resultingly, a highly efficient semiconductor apparatus can be provided. <IMAGE> <IMAGE> <IMAGE>

IPC 1-7

H01L 29/423; H01L 29/78

IPC 8 full level

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CPC (source: EP)

H01L 29/66621 (2013.01); **H01L 29/78** (2013.01)

Citation (search report)

- [XA] PATENT ABSTRACTS OF JAPAN vol. 017, no. 640 (E - 1465) 26 November 1993 (1993-11-26) & US 5338950 A 19940816 - BAHL INDER J [US]
- [XA] PATENT ABSTRACTS OF JAPAN vol. 016, no. 255 (E - 1214) 10 June 1992 (1992-06-10)
- [XA] PATENT ABSTRACTS OF JAPAN vol. 011, no. 117 (E - 498) 11 April 1987 (1987-04-11)
- [XA] PATENT ABSTRACTS OF JAPAN vol. 016, no. 322 (E - 1233) 14 July 1992 (1992-07-14)
- [A] PATENT ABSTRACTS OF JAPAN vol. 009, no. 126 (E - 318) 31 May 1985 (1985-05-31)

Cited by

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