

Title (en)

Circuit for the limitation of distortions of audio signals.

Title (de)

Schaltungsanordnung zur Begrenzung von Verzerrungen von Audiosignalen.

Title (fr)

Circuit de limitation de distortions de signaux audio.

Publication

EP 0655832 A3 19960131 (DE)

Application

EP 94113173 A 19940824

Priority

DE 4340167 A 19931125

Abstract (en)

[origin: EP0655832A2] In the case of a circuit arrangement for limiting levels of distortion of audio signals, which levels of distortion are produced during reproduction in a radio receiver having at least one output stage, a signal which characterises a predetermined degree of modulation can be picked off at the output stage. The signal which is conducted via an integration element forms a voltage describing the harmonic distortion factor (total harmonic distortion) and, by means of a predetermined characteristic, a manipulated variable which is forwarded to an actuator for amplifying the audio signals. <IMAGE>

IPC 1-7

H03G 3/30

IPC 8 full level

H03G 3/30 (2006.01)

CPC (source: EP)

H03G 3/3026 (2013.01)

Citation (search report)

- [A] EP 0455974 A2 19911113 - LICENTIA GMBH [DE]
- [A] EP 0295497 A2 19881221 - SGS THOMSON MICROELECTRONICS [IT]
- [A] US 4581589 A 19860408 - IKOMA TSUNEO [JP]

Designated contracting state (EPC)

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DOCDB simple family (publication)

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