

Title (en)

IMAGE PROCESSING DEVICE AND METHOD THEREFOR, AND GAME MACHINE HAVING IMAGE PROCESSING PART.

Title (de)

BILDVERARBEITUNGSVERFAHREN UND VORRICHTUNG, UND SPIELMASCHINE MIT EINEM BILDVERARBEITUNGSTEIL.

Title (fr)

DISPOSITIF ET PROCEDE DE TRAITEMENT D'IMAGES ET MACHINE DE JEU DOTEE D'UN ORGANE DE TRAITEMENT D'IMAGES.

Publication

EP 0660298 A1 19950628 (EN)

Application

EP 94919832 A 19940630

Priority

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- JP 9401066 W 19940630

Abstract (en)

In an access circuit (31) of a background picture generating part (22), cycle patterns are stored in an access register (40). Access commands read out are converted into control signals by a decoder (41). Addresses on a VRAM (12) which are assigned to necessary image data are generated by an address selector (42), and are fed to the VRAM (12). The image data are read from the VRAM (12) according to the control signals and the addresses, and they are stored in registers (33-36). Image data are read from the registers (33-36) by output circuits (37, 38), and picture element data are generated and outputted from the output circuits (37, 38). Thereby, alteration of the cycle patterns of the VRAM access can be treated flexibly which are resulted from alteration of display setting such as of the number of colors of the image data, the reduction ratios and the access frequencies. Further, according to the quantities of the image data and the access frequencies of the respective background pictures, the storages of the image data can be adjusted between a plurality of the VRAMs. <IMAGE>

IPC 1-7

G09G 5/36; **G06T 1/20**

IPC 8 full level

G06F 19/00 (2011.01); **G09G 5/393** (2006.01); **G09G 5/42** (2006.01)

CPC (source: EP KR)

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Cited by

US8233770B2; US8565575B2; US8625962B2; US8625966B2; US8792026B2; US8798440B2

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