

Title (en)

COINCIDENT ACTIVATION OF PASS TRANSISTORS IN A RANDOM ACCESS MEMORY.

Title (de)

KOINZIDIERENDE AKTIVIERUNG VON PASSIERTRANSISTOREN IN EINEM DIREKTZUGRIFFSPEICHER.

Title (fr)

ACTIVATION COINCIDENTE DE TRANSISTORS DE PASSE DANS UNE MEMOIRE A ACCES SELECTIF.

Publication

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Application

EP 93921284 A 19930830

Priority

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- US 94029992 A 19920903

Abstract (en)

[origin: WO9406120A1] The pass transistors in a random access memory array are activated only upon coincident (simultaneous) selection of both the associated row and the associated column of the memory cell; otherwise, activation of the pass transistors is prevented. Thus, when a word line is selected, only the pass transistors in the memory cell corresponding to a simultaneously selected bit line is active, rather than all of the pass transistors pairs connected to the word line. Transient power consumption during word line selection and deselection is thereby reduced. Coincident pass transistor activation may be obtained by providing a column select line for each column of the memory array, and gating means in each cell which electrically activates the associated pass transistors only upon simultaneous selection of the associated column select line and the associated word line, and for preventing activation of the associated pass transistors otherwise. When the column select lines and gating means are used, shared bit lines may be provided in the array. A single shared bit line may be used between adjacent columns of cells since only one of the columns will be selected by the column select line. A high density memory design is therefore provided.

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G11C 11/412

IPC 8 full level

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CPC (source: EP KR)

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