

Title (en)
Semiconductor device.

Title (de)
Halbleitervorrichtung.

Title (fr)
Dispositif à semi-conducteurs.

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Application
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Priority
BE 9400108 A 19940131

Abstract (en)
The invention relates to a semiconductor memory with a semiconductor body which is provided at a surface with a system of memory elements arranged in rows and columns. For addressing, the surface is provided with a system of mutually adjacent parallel selection lines 4, each coupled at one end to a selection transistor 19 with which the connection between the selection line and peripheral electronics can be opened or closed. These transistors are thin-film transistors which are formed, for example, in the selection lines themselves. As a result of this, the selection lines, and thus also the memory elements in the matrix, can be provided with minimum pitch. <IMAGE>

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H01L 23/525; **H01L 27/112**

IPC 8 full level
H01L 21/8247 (2006.01); **H01L 23/525** (2006.01); **H01L 27/10** (2006.01); **H01L 27/105** (2006.01); **H01L 27/115** (2006.01); **H01L 29/78** (2006.01); **H01L 29/786** (2006.01); **H01L 29/788** (2006.01); **H01L 29/792** (2006.01)

CPC (source: EP KR US)
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C-Set (source: EP US)
H01L 2924/0002 + **H01L 2924/00**

Citation (search report)
• [A] EP 0522539 A2 19930113 - NEC CORP [JP]
• [DA] US 4881114 A 19891114 - MOHSEN AMR M [US], et al
• [A] EP 0323078 A2 19890705 - ACTEL CORP [US]
• [A] EP 0367152 A2 19900509 - CASIO COMPUTER CO LTD [JP]
• [A] PATENT ABSTRACTS OF JAPAN vol. 5, no. 78 (E - 58) 22 May 1981 (1981-05-22)

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